

N-Ch MOSFET

General Description

The WSR150N04 uses advanced trench technology and design to provide excellent RDS(ON) with low gate charge. It can be used in a wide variety of applications.

Product Summery

BVDSS	RDSON	ID
40V	4mΩ	150A

Application

- Load switch
- Battery protection
- Uninterruptible power supply

Features

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}

Absolute Maximum Ratings

- Excellent package for good heat dissipation
- Special process technology for high ESD capability

TO-220AB Pin Configuration

Symbol Units Parameter Rating **Drain-Source Voltage** 40 V V_{DS} ± 20 v Gate-Source Voltage V_{GS} Continuous Drain Current, V_{GS} @ 10V¹ 150 I_D@T_C=25℃ А Continuous Drain Current, V_{GS} @ 10V¹ 98 I_D@T_C=100℃ А Pulsed Drain Current² 600 А I_{DM} Single Pulse Avalanche Energy³ EAS 350 mJ Total Power Dissipation⁴ w P_D@T_C=25℃ 180 °C $T_J T_{STG}$ **Operating Junction Temperature Range** -55 to 150

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
R _{eja}	Thermal Resistance Junction-Ambient ¹		50	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹		0.7	°C/W



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Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	40			V
$\triangle BV_{DSS} / \triangle T_J$	BV _{DSS} Temperature Coefficient	Reference to 25 $^\circ\!\!{\rm C}$, I_D=1mA		0.057		V/℃
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =30A		3	4	mΩ
V _{GS(th)}	Gate Threshold Voltage	$V_{GS}=V_{DS}$, I _D =250uA	2.0	3.0	4.0	V
$ riangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient			-5.68		mV/℃
I _{DSS}	Drain-Source Leakage Current	V_{DS} =40V , V_{GS} =0V , T _J =25 $^\circ \! \mathbb{C}$			1	uA
	Drain-Source Leakage Current	$V_{\text{DS}}\text{=}40\text{V}$, $V_{\text{GS}}\text{=}0\text{V}$, $T_{\text{J}}\text{=}55^\circ\!\!\!\mathrm{C}$			5	
I _{GSS}	Gate-Source Leakage Current	V_{GS} = $\pm20V$, V_{DS} =0V			±100	nA
gfs	Forward Transconductance	V _{DS} =5V , I _D =15A		40		S
Rg	Gate Resistance	V_{DS} =0V , V_{GS} =0V , f=1MHz		0.8		Ω
Qg	Total Gate Charge (4.5V)	V _{DS} =20V , V _{GS} =10V , I _D =30A		80		
Q _{gs}	Gate-Source Charge			17		nC
Q _{gd}	Gate-Drain Charge			21		
T _{d(on)}	Turn-On Delay Time	$ \begin{array}{c} & {\sf V}_{\rm DS}{=}20{\sf V}\;, {\sf V}_{\rm GS}{=}10{\sf V}\;, \\ & {\sf I}_{\rm D}{=}30{\sf A}\;, \;\; {\sf Rg}{=}1\Omega. \end{array} $		21		
Tr	Rise Time			32		
T _{d(off)}	Turn-Off Delay Time			71		ns
T _f	Fall Time			40		
Ciss	Input Capacitance	V _{DS} =20V , V _{GS} =0V , f=1MHz		4950		
C _{oss}	Output Capacitance			530		pF
C _{rss}	Reverse Transfer Capacitance			321		

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
ls	Continuous Source Current ^{1,6}	$V_G = V_D = 0V$, Force Current			150	А
I _{SM}	Pulsed Source Current ^{2,6}				600	А
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =30A , TJ=25℃			1.2	V
t _{rr}	Reverse Recovery Time	IF=20A ,dl/dt=100A/µs,TJ=25℃		27		nS
Qrr	Reverse Recovery Charge			47		nC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, $t \le 10$ sec.

- **3.** Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production
- **5.** E_{AS} condition: Tj=25 $^{\circ}$ C,V_{DD}=20V,V_G=10V,L=0.5mH,Rg=25 Ω



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1.2

Typical Characteristics

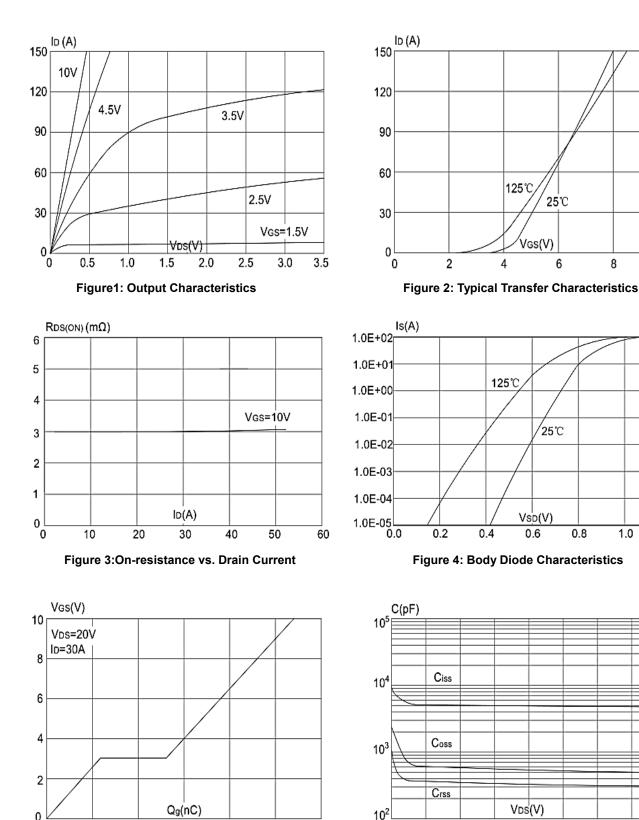


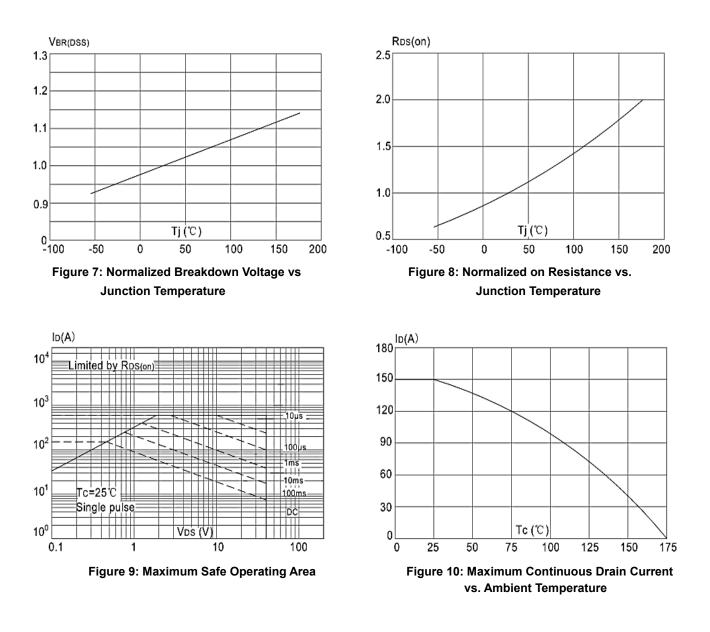
Figure 5: Gate Charge Characteristics

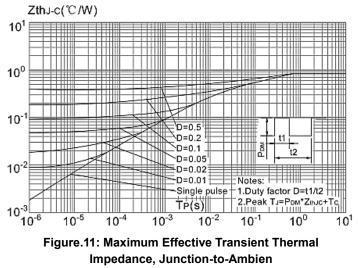
Figure 6: Capacitance Characteristics



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